

COMPONENT-MOUNTING METHOD AND COMPONENT-MOUNTING APPARATUS

BACKGROUND OF THE INVENTION

5 [0001] The present invention relates to a method for mounting components such as electronic components, ~~etc.~~ and the like on a circuit-formed substrate such as an electronic circuit board, and a component-mounting apparatus for ~~carrying out the~~ performing this component-mounting method.

10 [0002] Fig. 6 shows an outline of ~~a whole of a~~ conventional component-mounting apparatus (1) in its entirety. In Fig. 6, the component-mounting apparatus (1) comprises, as main components, ~~;~~ a component-feeding unit (2) composed of a cassette type component-feeding device for feeding components such as electronic components or the like; a tray-feeding unit (3) composed of a tray type component-feeding device; a mounding head (4) equipped with a plurality of nozzles for taking components out of both feeding units (2, 3) and mounting them ~~on~~ onto a circuit-formed substrate; an XY robot (5) for carrying the mounting head (4) to a predetermined position; a component-recognition camera (6) for recording and recognizing ~~the a~~ condition of a component held by a nozzle of the mounting head (4); a circuit-formed substrate-securing device (7) for carrying the circuit-formed substrate to the component-mounting apparatus (1) and securing the same; and a control unit (9) for controlling ~~the~~ operations of a ~~whole of the~~ component-mounting apparatus in its entirety.

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[0003] With reference to Fig. 6, a cassette type component-feeding device (8) having a reel, onto which a lot of components are tape-like wound up, is set on the component-feeding unit (2). A tray pallet type component-feeding device (10), on which a lot of components are arrayed, is set on the tray-feeding unit (3). The mounting head (4) is equipped with nozzle heads

(11) each having a nozzle (12) for sucking and ~~taking~~ removing a component (13) ~~out of~~ from the component-feeding unit (2) or the tray-feeding unit (3). ~~The~~ An angle of each nozzle (12) can be corrected by rotating the nozzle on an axis Z by ~~means~~ use of a rotation-controlling mechanism (θ rotation). The X-Y robot (5) carries the mounting head (4) on a plane in X- and
 5 Y-directions. The circuit-formed substrate-securing device (7) carries and secures the circuit-formed substrate (14) such as an electronic circuit substrate or the like. The mounting head (4) is equipped with a substrate-recognition camera (15) for recording and recognizing ~~the~~ a condition of the circuit-formed substrate when secured.

[0004] The component-mounting apparatus (1) thus constructed is operated as follows.

10 The mounting head (4) moves just above a component (13) fed by the component-feeding unit (2) or the tray-feeding unit (3), and causes each of the nozzles (12) to lower ~~to contact~~ such that one of the nozzles contacts and ~~suck~~ sucks the component (13) and ~~take~~ removes it ~~out of~~ from the component-feeding unit (2) or the tray-feeding unit (3), utilizing a negative pressure. Next, the mounting head (4), sucking and holding the component (13) ~~at each~~ with the one of the
 15 nozzles (12), is carried by the X-Y robot (5) to a position facing to the component-recognition camera (6). The component-recognition camera (6) records and recognizes the component (13) sucked and held by ~~each~~ nozzle (12) of the mounting head (4) while the mounting head (4) is passing through ~~the~~ a position facing ~~to~~ the component-recognition camera (6) at a predetermined speed. ~~The~~ An inclination of the component (13) and a dislocation of ~~the~~ a
 20 position thereof are measured based on ~~the~~ a result of the above recognition.

[0005] The circuit-formed substrate (14) is carried by the circuit-formed substrate-securing device (7) and then regulated and secured at a predetermined position. When the mounting head (4) is moved to a position facing ~~to~~ the circuit-formed substrate (14), the substrate-recognition camera (15) provided on the mounting head (4) records and recognizes the

circuit-formed substrate (14). ~~The~~ An inclination or dislocation of the circuit-formed substrate (14) is measured based on ~~the~~ a result of ~~the~~ this recognition. The control unit (9) ~~instructs~~ sends, to each of the nozzle heads (11) mounted on the mounting head (4), a correction amount of the position and inclination of the component (13) based on the position, inclination and dislocation of the circuit-formed substrate (14). ~~Each of the~~ The nozzle heads (11) ~~of the one nozzle~~ corrects ~~the~~ a position and inclination of the component (13) according to ~~the~~ this instruction, and then mounts the component (13) at a predetermined position on the circuit-formed substrate (14).

[0006] Fig. 7A is a view through the substrate-recognition camera (15) provided on the mounting head (4), showing ~~the~~ a condition of the circuit-formed substrate (14) when regulated and secured. In this regard, one sheet of circuit-formed substrate (14) may compose a single electronic circuit substrate. However, in association with recent electronic devices with small sizes and light weight, downsizing of electronic circuit ~~substrate~~ substrates is demanded, and thus, in many cases, a single circuit-formed substrate (14) is sectioned to provide a plurality of electronic circuit substrates as shown in Fig. 7A. In the example shown in Fig. 7A, the circuit-formed substrate (14) is sectioned for ~~9~~ nine individual substrates (16a) to (16i) which are arrayed in ~~3~~ three rows and ~~3~~ three columns. It may be sectioned for more individual substrates, for example, several tens of substrates. In the present specification, ~~a whole of one~~ entire sheet with an original size is referred to as the circuit-formed substrate (14), and any of specified and individual substrates (16a) to (16i) is denoted by using an individual reference number or notation. Further, when not a specified individual substrate but ~~the~~ a plurality of individual substrates provided from one circuit-formed substrate are generally referred to, such individual substrates are called individual substrates (16).

[0007] As shown in Fig. 7A, generally, a pair of reference marks (21) are provided at and around ~~the~~ corners on a diagonal line of the circuit-formed substrate (14). The substrate-

recognition camera (15) recognizes both reference marks (21) of the circuit-formed substrate (14) while being regulated and secured by the circuit-formed substrate-securing device (7), and ~~the~~ an inclination of the circuit-formed substrate (14) and dislocation of ~~the~~ a position thereof are measured based on ~~the~~ a result of ~~the~~ this recognition. The inclination of the circuit-formed substrate (14) and the dislocation of the position thereof are included in ~~the~~ correction amounts for the inclination of the component (13) and the dislocation of the position thereof when the component (13) is mounted.

[0008] On the other hand, generally, a pair of individual substrate marks (22) are provided at and around ~~the~~ corners of a diagonal line of each of the individual substrates (16). The component (13) itself becomes smaller in association with ~~the~~ downsizing of electronic devices as mentioned above, and thus, ~~the~~ a component-mounting density becomes higher. Therefore, it is required to accurately mount ~~the~~ components at predetermined positions without any interference with from other components which have already been mounted. The individual substrate marks (22) are used to ~~make~~ perform accurate positioning of components on each individual substrate (16).

[0009] In addition to the individual substrate marks (22), a position for indicating a bad mark (23) is provided on the individual substrate (16). If some factors for failure, such as incorrect mounting or non-mounting, occur on a specified individual substrate (16) ~~in~~ during any step of ~~the~~ a process of mounting components ~~on~~ onto the circuit-formed substrate (14), a bad mark (23) is indicated on ~~the~~ a relevant individual substrate (16). Generally, an operator or an automatic machine ~~puts~~ provides a bad mark (23) by coloring it using a black ink or the like when finding a failure in the course of an intermediate inspection step or the like. This bad mark (23) is recognized by the substrate-recognition camera (15) based on ~~the~~ an occupation ratio of brightness (white and black are grasped based on their proportion by a binary value level). An

individual substrate (16) attached with the bad mark (23) does not undergo a later component-mounting process so as to save useless consumption of components and loss of tact time.

[0010] ~~The arrows~~Arrows of the ~~a~~a broken line shown in Fig. 7A indicate the ~~a~~a passage along which the substrate-recognition camera (15) recognizes the ~~bad~~bad marks (23). ~~The~~This recognition passage starts from the ~~a~~a recognition of the bad mark (23) on the individual substrate (16a), followed by the bad marks (23) on other individual substrates (16b, 16c) ~~on~~in the same row, further followed by the bad mark (23) on the individual substrate (16d) in the ~~a~~a next row, and the recognition is ~~carried out~~performed in the same manner up to the final individual substrate (16i). In the example shown in Fig. 7A, bad marks (23) are put on the individual substrates (16a, 16c, 16e, 16h, 16i), respectively. Fig. 7B shows the ~~a~~a passage of recognizing the individual substrate marks (22) after the recognition of the bad marks (23). Also, in this recognition passage, as indicated by the arrows of the broken lines, first, a pair of the individual substrate marks (22) of the individual substrate (16a) are recognized, followed by the individual substrate marks (22) of the individual substrates (16b) to (16i) in order.

[0011] Fig. 8 shows a flowchart of a recognition operation performed by the substrate-recognition camera (15). In Fig. 8, the substrate-recognition camera (15) is moved to a position facing ~~to~~the circuit-formed substrate (14) in accordance with the movement of the mounting head (~~44~~4), and the substrate-recognition camera (15) first recognizes the reference marks (21) at ~~2~~two positions of the circuit-formed substrate (14) at Step 51. ~~In~~During an actual recognition operation, the substrate-recognition camera (15) first takes up the ~~an~~an image of the reference mark (21) at the ~~a~~a first point into CCD. This image is inputted to the control unit (9) and stored therein. Next, the camera (15) recognizes the ~~an~~an image of the reference mark (21) at the ~~a~~a second point and takes it into CCD and inputs this image to the control unit (9) and stores it therein. ~~The~~An inclination of the circuit-formed substrate (14) and a dislocation of the ~~a~~a position thereof are

measured based on the ~~a~~ result of the recognition of both reference marks (21) at the two points. Next, at Step 52, the camera (15) sequentially recognizes the bad marks (23) on the individual substrates (16) provided by sectioning the circuit-formed substrate (14) (~~nine~~ total 9-points in the example shown in ~~Fig. 7~~ Figs. 7A and 7B). As mentioned above, the data of the individual substrates (16) on which the bad marks (23) have been recognized are inputted to the control unit (9) so as not to undergo a later component-mounting step.

[0012] Next, at Step 53, the substrate-recognition camera (15) sequentially recognizes the paired individual substrate marks (22) on overall individual substrates (16) provided by sectioning the circuit-formed substrate (14) (~~18-eighteen~~ points in total in the example shown in Fig. 7 Figs. 7A and 7B). ~~The results~~ Results of the ~~this~~ recognition of the individual substrate marks (22) are inputted to the control unit (9) so as to be reflected on correction amounts for an inclination and a position of a component to be mounted ~~in the~~ during a later component-mounting step. After that, the component-mounting operation is ~~carried out~~ performed at Step 54, and the ~~component-components~~ (13) sucked by each of the nozzles is mounted on a predetermined position of each of the individual substrates (16).

[0013] However, the conventional component-mounting method as mentioned above has problems as follows. That is, ~~in during~~ the operation of recognizing the individual substrates (16), first, ~~the~~ recognition of the bad marks (23) ~~are carried out~~ is performed (Step 52 of the flowchart shown in Fig. 8), followed by ~~the~~ recognition of the individual substrate marks (22) (Step 53 of the same flowchart), and therefore, a lot of many recognition operations as a whole are required, and ~~long~~ much time is required for ~~such a lot of~~ performance of these many recognition operations, which may adversely influence on a case of a circuit-formed substrate (14) which is sectioned into several tens of individual substrates. For example, in case of a circuit-formed substrate (14) sectioned into ~~77~~ seventy-seven individual substrates, as many as

~~231 times of two hundred thirty one~~ recognition operations in total are required for a single circuit-formed substrate (14).

[0014] Next, the inclination of the circuit-formed substrate (14) and dislocation of the position thereof are recognized based on ~~the results of the recognition of~~ a pair of reference marks (21) on the circuit-formed substrate (14), and ~~the results of this recognition are used in the~~ calculation of ~~the correction amounts for the inclination of the component (13) and the a position~~ thereof. However, a recognition error may occur in the course of the operation of recognizing the individual substrate marks (22) on each of the individual substrates (16), depending on ~~the a~~ degree of ~~the inclination of the circuit-formed substrate (14), and thus, in some cases, the a result~~ of recognition of the reference marks (21) of the circuit-formed substrate is not effectively utilized. Fig. 9 shows one of such situations, in which, as indicated by ~~the an~~ arrow of the broken line (25), a pair of reference marks (21) of the circuit-formed substrate (14) ~~are is~~ first recognized by the substrate-recognition camera (15), and the inclination of the circuit-formed substrate (14) and dislocation of the position thereof are measured based on ~~the results of the this~~ recognition. These results are used for calculation of correction amounts for the inclination of a component (13) to be mounted and dislocation of the position thereof.

[0015] In review of each of the individual substrates (16), for example, ~~the recognition of~~ the individual substrate marks (22) of the individual substrates (16a, 16b) has no failure because the individual substrate marks (22) of the individual substrates (16a, 16b) are included in the visual field (31) of the substrate-recognition camera (15). In contrast, for example, in case of ~~the~~ individual substrate (16c), a component of ~~the individual substrate mark (22) indicated by the a~~ circle is excluded from the visual field (31) of the camera (15) indicated by ~~the a~~ square, which results in a recognition error. Such recognition errors similarly occur in case of the individual substrates (16f, 16g, 16h, 16i). If such a recognition error occurs, the following process may be

optionally determined. However, correction amounts for the inclination and position of a component (13) ~~can-not~~cannot be determined if such a recognition error is left ~~unsolved~~unresolved. Therefore, the individual substrates (16) having such recognition errors have conventionally been judged as defectives. The individual substrates (16) judged as defectives are not subjected to ~~the-a~~ following component-mounting step. In other words, the individual substrates (16) which may be originally non-defectives are judged as defectives and are scrapped, depending on ~~the~~ inclination of the circuit-formed substrate (14).

[0016] To solve the problem induced by the above recognition error, it is proposed to widen the visual field of the substrate-recognition camera (15). However, this solution has a problem in that, generally, ~~the~~ resolution of the camera degrades if the visual field of the camera is widened, which leads to a further problem in that ~~the~~ tact time becomes longer because the recognition of an individual substrate requires a longer time. In addition, there is a danger of degrading ~~the~~ accuracy of recognition determined by ~~the-an~~ occupation rate of brightness mentioned above, because, by widening the visual field of the camera (15), other factors may be included in the visual field of the camera (15) and because such factors may be recognized by mistake. At present, on the contrary, there is a tendency of narrowing the visual field of a recognition camera to improve ~~the~~ resolution of the camera and to thereby reduce recognition time, so as to improve production efficiency. However, narrowing the visual field means more frequent occurrence of the foregoing recognition errors, which leads to a decrease in ~~the-a~~ yield of non-defectives.

[0017] Objects of the present invention are, therefore, to provide a component-mounting apparatus which is free from the above problems ~~in-the~~ during recognition operations of the circuit formed substrate (14) of the conventional apparatus, and which can ~~carry-out~~ perform efficient recognition operations to increase ~~the-a~~ yield of non-defectives and to thereby improve

the productivity, and to provide a component-mounting method.

SUMMARY OF THE INVENTION

[0018] The present invention provides a method of mounting a component, which
5 comprises ~~the steps of:~~ recognizing a bad mark which is indicated on a circuit-formed substrate
when each of at least one individual substrate provided by sectioning the circuit-formed substrate
includes a defective individual substrate, and recognizing an individual substrate mark which is
provided on the circuit-formed substrate so as to recognize ~~the~~ a position and ~~the~~ an inclination
of each of at least one individual substrate as above; and mounting a component ~~on~~ onto the
10 circuit-formed substrate, aiming at an individual substrate having no bad mark indicated; and the
invention is characterized in that the bad mark is indicated on the individual substrate mark.
This method is effective to improve ~~the~~ efficiency of ~~the~~ a recognition operation by using the
individual substrate mark also as the bad mark.

[0019] Another aspect of the present invention provides a method for mounting a
15 component, which comprises ~~the steps of:~~ recognizing ~~the~~ a condition of a sucked component
which is fed by a component-feeding device, sucked and ~~taken out~~ removed therefrom;
recognizing ~~the~~ a condition of a secured circuit-formed substrate which is carried, regulated and
secured; recognizing ~~the~~ a position and ~~the~~ an inclination of at least one individual substrate
provided by sectioning the circuit-formed substrate; calculating correction amounts for ~~the~~ a
20 position and inclination of the component, which is to be mounted, based on ~~the~~ a result of the
recognition of the component-sucking condition, ~~the~~ a result of ~~the~~ recognition of the circuit-
formed substrate-securing condition, and ~~the~~ a result of ~~the~~ recognition of the position and
inclination of the individual substrate; and making necessary correction on the component based
on the result of the above calculation, and mounting the component at a predetermined position

on the individual substrate; and the invention is characterized in that a mark which is provided on the individual substrate so as to recognize the position and inclination of the individual substrate is used also as a bad mark for discriminating a defective individual substrate. This method is effective to reduce ~~the~~a number of the recognition operations performed by the substrate-recognition camera, by using the individual substrate mark also as the bad mark.

[0020] Another aspect of the present invention is characterized in that the bad mark is indicated by coloring the individual substrate mark of an individual substrate which is judged as a defective before mounting a component.

[0021] Another aspect of the present invention provides a method for mounting a component, which comprises ~~the~~ steps of: recognizing ~~the~~a condition of a sucked component which is fed from a component-feeding unit, sucked and ~~taken-out~~ removed therefrom; recognizing ~~the~~a condition of a secured circuit-formed substrate which is carried, regulated and secured; recognizing ~~the~~a position and inclination of at least one individual substrate provided by sectioning the circuit-formed substrate; calculating correction amounts for ~~the~~a position and inclination of the component, which is to be mounted, based on ~~the~~ results of the recognition of the component-sucking condition, the circuit-formed substrate-securing condition, and the position and inclination of the individual substrate; and making necessary correction ~~on~~of the component based on the result of the above calculation, and mounting the component at a predetermined position on the individual substrate, and the invention is characterized in that a position at which a substrate-recognition camera should recognize the position and inclination of the individual substrate is controlled based on the result of the recognition of the circuit-formed substrate-securing condition. This method is effective to avoid occurrence of a recognition error by utilizing the inclination of the circuit-formed substrate and the dislocation of the position thereof for the recognition operation of the individual substrate.

[0022] Another aspect of the present invention provides a method for mounting a component, which comprises the steps of: recognizing the a condition of a sucked component which is fed from a component-feeding unit, sucked and ~~taken-out~~ removed therefrom; recognizing the a condition of a secured circuit-formed substrate which is carried, regulated and secured; recognizing the a position and inclination of at least one individual substrate provided by sectioning the circuit-formed substrate; calculating correction amounts for the a position and inclination of the component, which is to be mounted, based on the results of the recognition of the component-sucking condition, the circuit-formed substrate-securing condition, and the position and inclination of the individual substrate; and making necessary correction ~~on~~ of the component based on the result of the above calculation, and mounting the component at a predetermined position on the individual substrate; and the invention is characterized in that, when a portion or a whole of a mark provided on the circuit-formed substrate so as to recognize the circuit-formed substrate-securing condition, or a mark provided on the individual substrate so as to recognize the condition of the individual substrate, is not included within the a visual field of a substrate-recognition camera for recognizing these marks, the a position of the mark is detected and the mark is again recognized. This method is effective to improve the yield of the non-defectives by detecting the position of the mark and again recognizing the same mark, even if a recognition error occurs.

[0023] Another aspect of the present invention is characterized in that the a position of the mark is detected based on a ~~portion~~ portion of the mark captured within the visual field of the substrate-recognition camera, and that the mark is again recognized by moving the visual field of the substrate-recognition camera to the this detected position.

[0024] Another aspect of the present invention is characterized in that the a position of the mark is detected by enlarging the visual field of the substrate-recognition camera, and that

the mark is again recognized.

[0025] Another aspect of the present invention provides a component-mounting apparatus which comprises: a component-feeding unit for feeding a component to be mounted; a mounting head for ~~taking~~ removing the component ~~out of~~ from the component-feeding unit and mounting it on a circuit-formed substrate; a component-recognition camera for recognizing ~~the~~ a condition of the component when held by the mounting head; an X-Y robot for carrying the mounting head to a predetermined position; a circuit-formed substrate-securing device for carrying and securing the circuit-formed substrate; a substrate-recognition camera for recognizing ~~the~~ a condition of the secured circuit-formed substrate; and a control unit for controlling ~~the~~ overall operations of the apparatus. With the above construction, the substrate-recognition camera recognizes an individual substrate mark which is provided on each of at least one individual substrate provided by sectioning the circuit-formed substrate so as to recognize ~~the~~ a position and inclination of the individual substrate; correction amounts for the position and inclination of the component to be mounted are calculated based on ~~the~~ a result of ~~the~~ this recognition of the individual substrate mark, ~~the~~ a result of ~~the~~ recognition of the component-holding condition performed by the component-recognition camera, and ~~the~~ a result of the recognition of the circuit-formed substrate-securing condition performed by the substrate-recognition camera so as to make necessary correction ~~on~~ of the component; and the mounting head is carried by the X-Y robot so as to mount the component at a predetermined position on the individual substrate; and the invention is characterized in that a bad mark to be indicated when the circuit-formed substrate includes a defective individual substrate is put on an individual substrate mark of the defective individual substrate so that the substrate-recognition camera can recognize the bad mark at the same time when recognizing the individual substrate mark. ~~The use~~ Use of this apparatus is effective to improve the efficiency of ~~the~~ a recognition operation by

using the individual substrate mark also as the bad mark.

[0026] Another aspect of the present invention provides a component-mounting apparatus which comprises: a component-feeding unit for feeding a component to be mounted; a mounting head for ~~taking-removing~~ the component ~~out of~~ from the component-feeding unit and mounting it on a circuit-formed substrate; a component-recognition camera for recognizing ~~the a~~ condition of the component when held by the mounting head; an X-Y robot for carrying the mounting head to a predetermined position; a circuit-formed substrate-securing device for carrying and securing the circuit-formed substrate; a substrate-recognition camera for recording and recognizing ~~the a~~ condition of the secured circuit-formed substrate; and a control unit for controlling ~~the overall~~ operations of the apparatus. With the above construction, the substrate-recognition camera recognizes an individual substrate mark which is provided on each of at least one individual substrate provided by sectioning the circuit-formed substrate so as to recognize ~~the a~~ position and inclination of the individual substrate; correction amounts for ~~the a~~ position and inclination of the component to be mounted are calculated based on ~~the a~~ result of the recognition of the individual substrate mark, ~~the a~~ result of ~~the~~ recognition of the component-holding condition by the component-recognition camera, and ~~the a~~ result of ~~the~~ recognition of the circuit-formed substrate-securing condition by the substrate-recognition camera so as to make necessary correction ~~on of~~ the component; and the mounting head is carried by the X-Y robot so as to mount the component at a predetermined position on the individual substrate; and the invention is characterized in that a position at which the substrate-recognition camera should recognize the individual substrate mark is controlled based on the result of the recognition of the circuit-formed substrate-securing condition. This apparatus makes it possible to avoid such a situation that the individual substrate mark is not recognized at all, as much as possible, by controlling ~~the a~~ position of the substrate-recognition camera based on the result of the

recognition of the position and inclination of the circuit-formed substrate.

[0027] Another aspect of the present invention provides a component-mounting apparatus which comprises a component-feeding unit for feeding a component to be mounted; a mounting head for ~~taking-removing~~ the component ~~out of~~ from the component-feeding unit and mounting it on a circuit-formed substrate; a component-recognition camera for recognizing ~~the~~ a condition of the component when held by the mounting head; an X-Y robot for carrying the mounting head to a predetermined position; a circuit-formed substrate-securing device for carrying and securing the circuit-formed substrate; a substrate-recognition camera for recording and recognizing ~~the~~ a condition of the secured circuit-formed substrate; and a control unit for controlling ~~the~~ overall operations of the apparatus. With the above construction, the substrate-recognition camera recognizes an individual substrate mark which is provided on each of at least one individual substrate provided by sectioning the circuit-formed substrate so as to recognize ~~the~~ a position and inclination of the individual substrate; correction amounts for ~~the~~ a position and inclination of the component to be mounted are calculated based on ~~the~~ a result of the recognition of the individual substrate mark, ~~the~~ a result of the recognition of the component-holding condition by the component-recognition camera, and the result of the recognition of the circuit-formed substrate-securing condition performed by the substrate-recognition camera so as to make necessary correction ~~on~~ of the component; and the mounting head is carried by the X-Y robot so as to mount the component at a predetermined position on the individual substrate; and the invention is characterized in that, when a portion or a whole of a reference mark provided on the circuit-formed substrate for recognizing the circuit-formed substrate-securing condition, or an individual substrate mark, is not included within ~~the~~ a visual field of the substrate-recognition camera, the substrate-recognition camera detects ~~the~~ a position of the mark and again recognizes the same mark.

[0028] Another aspect of the present invention is characterized in that ~~the~~a position of the mark is detected based on a portion of the mark captured within the visual field of the substrate-recognition camera, and that the mark is again recognized by moving the visual field of the substrate-recognition camera to ~~the~~this detected position.

5 [0029] Another aspect of the present invention is characterized in that ~~the~~a position of the mark is detected by enlarging ~~the~~a visual field of the substrate-recognition camera, and that the detected mark is again recognized.

BRIEF DESCRIPTION OF THE DRAWINGS

10 [0030] Figs. 1A and 1B are plan views of a circuit-formed substrate according to an embodiment of the present invention;

[0031] Fig. 2 is an ~~operation~~operational flowchart of a process of recognition of the circuit-formed substrate shown in Fig. 1;

15 [0032] Fig. 3 is a plan view of a circuit-formed substrate according to another embodiment of the present invention, illustrating a process of recognition thereof;

[0033] Figs. 4A and 4B are plan views of a circuit-formed substrate according to ~~other~~yet another embodiment of the present invention, illustrating a process of recognition thereof;

[0034] Fig. 5 is an ~~operation~~operational flowchart of ~~the~~a process of recognition of the circuit-formed substrate shown in ~~Fig. 4~~Figs. 4A and 4B;

20 [0035] Fig. 6 is a perspective view of a conventional component-mounting apparatus;

[0036] Figs. 7A and 7B are plan views of a conventional circuit-formed substrate, illustrating an example of process of recognition thereof;

[0037] Fig. 8 is an ~~operation~~operational flowchart of the process of recognition of the circuit-formed substrate shown in ~~Fig. 7~~Figs. 7A and 7B; and

[0038] Fig. 9 is a plan view of a conventional circuit-formed substrate, illustrating the problems in-associated with the process of recognition of the circuit-formed substrate.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

5 [0039] The present invention is described in detail by way of the following embodiments, which should not be construed as limiting the scope of the present invention in any way.

[0040] A component-mounting apparatus and a component-mounting method according to ~~the~~ a first embodiment of the present invention are described with reference to the accompanying drawings.

10 [0041] Figs. 1A and 1B show a circuit-formed substrate (14) and recognition marks (21, 22, 23) according to the present embodiment. In this connection, ~~the~~-like components or components in the present embodiment and ~~the~~-following embodiments are denoted by the same reference numbers or notations. ~~The-components~~ Components which are not included in the description of the present embodiment and the following embodiments are the same ones
15 included in the conventional component-mounting apparatus and the conventional component-mounting method.

[0042] In Figs. 1A and 1B, a pair of reference marks (21) are provided on a diagonal line
of the circuit-formed substrate (14), and individual substrate marks (22) are provided on the individual substrates (16a) to (16i), respectively. This constitution is the same as that of the
20 conventional apparatus.

[0043] The circuit-formed substrate (14) according to the present invention has no special position for a bad mark, and ~~the~~-individual substrate mark (22) is used also as a position on which a bad mark should be put. In detail, the individual substrate mark (22) is used to recognize ~~the~~ a condition of each individual substrate (16) as in the conventional apparatus, and

simultaneously used as ~~the a~~ position on which an operator or a machine will color or apply a bad mark when finding a defective factor such as incorrect mounting or non-mounting of a component on each individual substrate (16) in the course of the above recognition step.

[0044] Fig. 1A shows such a situation in which bad marks (23) are put on ~~the individual~~ substrates (16a, 16c, 16e, 16h, 16i) because some defective factors are found in ~~them~~ these substrates. ~~The bad~~ Bad mark (23) may be put on either of two individual substrate marks (22) of each individual substrate (16). Based on ~~the a~~ result of recognition of these bad marks (23) by ~~the a~~ substrate-recognition camera (15), components (13) are mounted on only ~~the individual~~ substrate-substrates (16b, 16d, 16f, 16g) on which the bad marks (23) are not applied, but not on the individual substrates (16) on which the bad marks (23) are ~~put~~ applied. The individual substrates (16) on which the components are mounted and the individual substrates (16) on which the components are not mounted will be ~~selected~~ grouped later into non-defectives and defectives, respectively.

[0045] Fig. 2 shows a flowchart for a recognition operation by the substrate-recognition camera (15). In Fig. 2, the substrate-recognition camera (15) moved to a position facing ~~to the~~ circuit-formed substrate (14) in accordance with the movement of ~~the a~~ mounting head (4), first, records and recognizes ~~the images of~~ a pair of reference marks (21) on the circuit-formed substrate (14) at Step 1, and ~~the an~~ inclination of the circuit-formed substrate (14) as a whole, and dislocation of ~~the a~~ position thereof, are measured based on this recognition. Next, the substrate-recognition camera (15) sequentially recognizes two individual substrate marks (22) on each of the individual substrates (16) at Step 2. As mentioned above, bad marks (23) are previously put on the individual substrate marks (22) of the individual substrates (16) having some defective factors. Therefore, at Step 2, not only ~~the conditions of~~ the individual substrates (16) but also the bad marks (23) of the individual substrates (16) having some defective factors

are concurrently recognized so as to distinguish ~~the~~ non-defectives from ~~the~~ defectives. Of the individual substrates (16) having no bad mark (23) found, ~~the~~ positions of ~~the~~ components (13) and ~~the~~ correction amounts for their inclination are calculated based on ~~the~~ a result of recognition of the individual substrate marks (22), and then, the components (13) are mounted on ~~the~~ predetermined positions of the individual substrates at Step 3.

[0046] In the above conventional apparatus, the recognition operation of the bad marks (23) and the recognition operation of the individual substrate marks (22) are separately ~~carried out~~ performed as described above. In contrast, according to the present embodiment, it becomes possible to save all ~~the~~ time required for ~~the~~ a recognition operation for the bad marks (23), which leads to large time reduction. Further, a defective individual substrate (16) can be discriminated at once if a bad mark (23) is put on either of the pair of individual substrate marks (22) which has been found earlier in one individual substrate (16), and ~~the~~ a recognition operation for the other individual substrate mark (22) becomes unnecessary, which leads to a further reduction of tact time. In this regard, although the present embodiment is described as using one circuit-formed substrate which ~~are~~ is sectioned for a plurality of individual substrates (16), it is also possible to apply the present embodiment to one circuit-formed substrate provided for a single individual substrate. In this case, the reference marks (21) of the circuit-formed substrate can be used as individual substrate marks (22).

[0047] Next, a component-mounting apparatus and a component-mounting method according to ~~the~~ a second embodiment of the present invention are described with reference to the accompanying drawings. As has been already described with reference to Fig. 9 for the conventional apparatus, when ~~the~~ inclination of the circuit-formed substrate (14) and dislocation of ~~the~~ a position thereof are recognized by ~~the~~ a recognition operation for ~~the~~ reference marks (21) of the circuit-formed substrate (14), ~~the~~ results of this recognition are reflected on only the

correction of the inclination of the a component (13) to be mounted and the a position of the component (13). If the inclination of the circuit-formed substrate (14) is large, the individual substrate marks (22) are not included within the visual field (31) of the substrate-recognition camera (15), which disadvantageously becomes a factor for a recognition error. Fig. 3 illustrates

5 a recognition operation for the circuit-formed substrate (14) according to the present embodiment.

In Fig. 3, the circuit-formed substrate (14) is supposed to incline an angle of α relative to a normal condition shown in ~~Figure~~ this figure as a result of the recognition of the reference marks (21) of the circuit-formed substrate (14) by the substrate-recognition camera (15). In the present embodiment, the ~~this~~ recognition result reflects on not only correction amounts for the ~~an~~ inclination and position of the component (13) but also a recognition operation for bad marks (23) by the substrate-recognition camera (15) (see Step 52 in Fig. 8), and a recognition operation for individual substrate marks (22) by the substrate-recognition camera (15) (see Step 53 in Fig. 8) at the a next step.

[0048] Thus, as indicated by the broken line (28) in Fig. 3, when recognizing the individual substrate marks (22), the substrate-recognition camera (15) moves to a position which is predetermined by reflecting the ~~above~~ inclination α of the circuit-formed substrate (14), so as to ~~carry out~~ perform a recognition operation for each of the individual substrates (16). Therefore, the individual substrate marks (22) can be easily captured within the visual field (31) of the substrate-recognition camera (15). This avoids a recognition error relative to the individual substrate mark (22), and further a ~~misjudge~~ misjudgment of a non-defective individual substrate as a defective based on such a recognition error. Thus, a component is mounted on a predetermined position of each individual substrate (16). That is, reflecting the recognition result of the circuit-formed substrate (14) on the recognition operations for the bad marks (23), and also the individual substrate marks (22) by the substrate-recognition camera (15), leads to an

improvement ~~on the~~ of yield of non-defectives and also to an increase in ~~the~~ production efficiency of the component-mounting process.

[0049] Since it becomes easy to avoid a recognition error by the substrate-recognition camera (15), it becomes possible to further narrow the visual field of the substrate-recognition camera (15), which provides a possibility to reduce ~~the~~ a tact time for a recognition operation in association with an improvement ~~on the~~ of resolution. Since ~~a lot of~~ many recognition operations are required for bad marks (23) and individual substrate marks (22) of one circuit-formed substrate (14), further reduction of ~~the~~ tact time by narrowing the visual field of the camera is particularly effective ~~to~~ for a circuit-formed substrate which is sectioned for far more individual substrates.

[0050] Although Fig. 3 shows the conventional circuit-formed substrate (14) which separately provides the bad marks (23) on the respective individual substrates (16), the bad marks (23) may be eliminated and the individual substrate marks (22) may be used also as the bad marks (23) as described in the component of the first embodiment.

[0051] Next, a component-mounting apparatus and a component-mounting method according to ~~the~~ a third embodiment of the present invention are described with reference to the accompanying drawings. In Fig. 4A, ~~the~~ visual field (31) of ~~the~~ substrate-recognition camera (15) is formed as a substantially right square around ~~the~~ center (32) of the camera. When the reference mark (21) of ~~the~~ circuit-formed substrate (14) is recognized by such a substrate-recognition camera (15), ~~a whole of the~~ reference mark (21) in its entirety is generally required to be included within the visual field (31). If a component of the reference mark (21) (~~the~~ hatched portion (33)) is excluded from the visual field (31) as shown in Fig. 4A, the conventional component-mounting apparatus would judge it as a recognition error. Since it is impossible to calculate a correction amount for ~~the~~ a position of the circuit-formed substrate (14) so as to ~~carry~~

~~perform out the~~ following component-mounting, such a circuit-formed substrate is scrapped as a defective. Also, ~~the~~ individual substrate (16) is scrapped in the same manner as a defective if a recognition error is found in the individual substrate mark (22).

[0052] On the other hand, where even a component of the reference mark (21) of the circuit-formed substrate (14) is included within the visual field (31) in the present embodiment, ~~the a~~ position of the reference mark (21) is detected based on such a component of the reference mark (21). In an example shown in Fig. 4A, ~~the~~ length "a" of one side of the reference mark (21) is known from ~~the a~~ shape of the reference mark (21), known in advance, and it is possible to measure ~~the~~ length "b" of a component of ~~the a~~ side captured within the visual field (31), based on which the portion (the hatched portion (33)) of the reference mark (21) excluded from the visual field (31) of the substrate-recognition camera (15) can be estimated. Therefore, it is also possible to estimate ~~the a~~ position of ~~the~~ center (34) of the reference mark (21), so that it becomes possible to calculate "x" and "y" as amounts of dislocation between this center (34) and the center (32) of the substrate-recognition camera (15).

[0053] In the present embodiment, as described above, recognition of only a component of the reference mark (21) ~~in the~~ during a first recognition operation is not judged as a recognition error, and the position of the reference mark (21) is detected and the amounts "x" and "y" of dislocation of the center position are calculated. Next, ~~the~~ positioning of the substrate-recognition camera (15) is again ~~done~~ performed by taking into account the above calculated dislocation amounts, and then, ~~the a~~ second recognition operation is ~~carried out~~ performed. By doing so, the reference mark (21) is set around the center (32) of the visual field (31) as shown in Fig. 4B. Widening the visual field of the substrate-recognition camera (15) to reduce ~~the a~~ number of recognition errors leads to an increase in tact time required for all ~~the~~ recognition operations, resulting in a lower production efficiency. In the above method according to the

present embodiment, it is sufficient to ~~carry out~~ perform the second recognition operation, aiming at only the reference mark (21) which would be conventionally judged as a recognition error. Thus, the above mentioned time loss caused by widening the visual field can be avoided, and the efficiency of the a recognition operation is improved.

5 [0054] Although the square marks are employed in the drawings, it is easy to detect the a center position of a mark to be recognized if such a mark has a shape of circle, triangle or the like, provided that the shape of this mark is previously known. Further, such detection of a mark can be applied to not only the reference mark (21) but also the individual substrate marks (22).

10 [0055] Fig. 5 shows the a flowchart of the recognition operation by the substrate-recognition camera (15) according to the present embodiment. In Fig. 5, the substrate-recognition camera (15) moved to a position facing to the circuit-formed substrate (14) in accordance with the movement of the mounting head (4), first, recognizes the reference mark (21) of the circuit-formed substrate (14) at Step 11. If normally recognizing the reference mark (21) at Step 11, the substrate-recognition camera (15) sequentially recognizes the individual
15 substrate marks (22) of all the individual substrates (16) on a plurality of sections of the circuit-formed substrate (14) at Step 12. ~~In~~ During this recognition operation, the conditions of the individual substrates (16) and discrimination of the non-defectives from the defectives thereof are recognized depending on the presence or absence of bad marks (23) described in the component of the first embodiment. If the individual substrate mark (22) can be normally
20 recognized at Step 12, the correction amounts for the inclination of the component (13) and the position thereof are calculated based on the recognition results at Steps 11 and 12. Then, the component (13) is mounted on a predetermined position at Step 13.

[0056] If only a component of the reference mark (21) of the circuit-formed substrate (14) is recognized at Step 11, the program goes to Step 16, at which the a position of the

reference mark (21) is detected and ~~the~~a dislocation amount from the substrate-recognition camera (15) is calculated. The substrate-recognition camera (15) moves based on ~~the~~a result of ~~the~~this calculation and ~~carries-out~~performs the second recognition operation relative to the reference mark (21) at Step 17. If a normal recognition operation is ~~done~~performed at Step 17, the program goes to Step 12, and then, the component-mounting operation is ~~carried-out~~performed following the above-mentioned flowchart.

[0057] If only a component of the individual substrate mark (22) is recognized at Step 12, the program goes to Step 18, at which ~~the~~a position of the individual substrate mark (22) is detected and ~~the~~an amount of dislocation from the substrate-recognition camera (15) is calculated. The substrate-recognition camera (15) moves based on ~~the~~a result of ~~the above~~this calculation and ~~carries-out~~performs the second recognition operation relative to the individual substrate mark (22) at Step 19. When a normal recognition operation is ~~carried-out~~performed at this step, necessary correction for the component is ~~done~~performed in the same manner as above, and the component-mounting operation is ~~carried-out~~performed at Step 13.

[0058] ~~The operation~~Operational flows indicated by ~~the~~broken lines on the left side of Fig. 5 optionally can be provided. In other words, even if the reference mark (21) of the circuit-formed substrate (14) ~~can not~~cannot be recognized at all, it is supposed that the reference mark (21) should be in the vicinity of the visual field (31). Therefore, the visual field (31) of the substrate-recognition camera (15) is enlarged at Step 21 and the second recognition operation is ~~carried-out~~performed at Step 22. ~~The time~~Time required for the recognition operation becomes longer in a case where the visual field is widened. However, this is done ~~on~~for only a circuit-formed substrate (14) in an abnormal condition under which the reference mark (21) ~~can not~~cannot be recognized at all. It is considered that, in some cases, it is more economically advantageous to again recognize such a circuit-formed substrate (14) by enlarging the visual

field of the camera rather than to scrap this circuit-formed substrate (14) as a defective. If the reference mark (21) can be normally recognized by widening the visual field, the program returns to Step 12 and proceeds according to ~~the normal operation~~ operational flow. In this regard, in a case where only a component of the reference mark (21) is recognized even after the visual field is enlarged at Step 21, the operations described at Steps 16 and 17 may be again repeated; that is, the a position of the mark is detected, and the reference mark is again recognized.

[0059] Similarly, in a case where the individual substrate mark (22) ~~can not~~ cannot be recognized at all ~~in the~~ during a recognition operation for the individual substrate mark (22) at Step 12, the program optionally may proceed to Step 23 to enlarge the visual field of the substrate-recognition camera (15) and again recognize the individual substrate mark (22) at Step 27. However, in this case, it is only one individual substrate (16) that can be remedied, and thus, the cost-effectiveness is relatively small as compared with ~~the~~ a case where ~~a whole of the~~ circuit-formed substrate (14) ~~are~~ in its entirety is remedied at Step 21. When the individual substrates (22) have been recognized by the operation of the above re-recognition, the program proceeds to Step 13 to correct ~~the~~ an inclination and position of the component (13) and mount it at a predetermined position.

[0060] The flowchart for the recognition operation shown in Fig. 5 is made for concurrent recognition of the individual substrate mark (22) and the bad mark (23) at Step 12. However, the individual substrate mark (22) and the bad mark (23) may be separately recognized as in the conventional apparatus. Otherwise, it is desirable that, as in the second embodiment, after the recognition of the reference mark (21) at Step 11, ~~the~~ a result of ~~the~~ this recognition is utilized to correct ~~the~~ a position of the substrate-recognition camera (15) in the course of recognizing the individual substrate mark (22) at Step 12.

[0061] The foregoing descriptions have been made for the component-mounting methods and the component-mounting apparatuses according to the embodiments of the present invention in which a plurality of individual substrates are provided from one circuit-formed substrate. However, as referred to in the component of the first embodiment, the scope of the present invention is not limited to the above circuit-formed substrate from which ~~the~~ a plurality of individual substrates are provided. The present invention also can be applied to a single substrate provided from one circuit-formed substrate ~~as well~~.

[0062] According to the component-mounting apparatuses and the component-mounting methods of the present invention, it is possible to decrease ~~the~~ a number of the recognition steps for the marks of a single or a plurality of individual substrate(s) provided from a circuit-formed substrate, and thus to save ~~the~~ tact time required for the recognition process and to improve the production efficiency.

[0063] Further, according to the component-mounting apparatuses and the component-mounting methods of the present invention, it is possible to reduce ~~the~~ a rate of causing recognition errors, and also it is possible to remedy a circuit-formed substrate or individual substrates which are conventionally scrapped as defectives and to provide them as non-defectives. Therefore, ~~the~~ yield of non-defectives obtained in the course of mounting components can be improved.

[0064] Further, according to the component-mounting apparatuses and the component-mounting methods of the present invention, since it is possible to reduce the rate of causing recognition errors, ~~the~~ resolution of the substrate-recognition camera can be improved by narrowing the visual field of the camera, so that ~~the~~ tact time required for the recognition process can be reduced, which leads to higher production efficiency.

[0065] It should be noted that this application is based upon ~~the~~ Japanese Patent

Application No. 2000-374428, which is entirely incorporated herein by reference.

ABSTRACT OF THE DISCLOSURE

The A number of recognition operations for the a circuit-formed substrate-(14) as a whole is reduced by concurrently recognizing a bad mark-(23) and an individual substrate mark (22) in the course of a recognition process of a single or a plurality of individual substrate(s) (16) provided by sectioning the circuit-formed substrate-(14). The results Results of the recognition of the an inclination and dislocation of the circuit-formed substrate (14) are used to control the a position of the a substrate-recognition camera (15) which recognizes the individual substrate-(15), thereby reducing the a rate of occurrence of recognition errors. When a component of the recognition marks (21) or (22) the individual substrate mark is captured within the a visual field (31) of the substrate-recognition camera-(15), the a position of the a corresponding one of these recognized marks (21) or (22) is specified, and such a mark is again recognized, and thus, the occurrence of a recognition error can be inhibited.